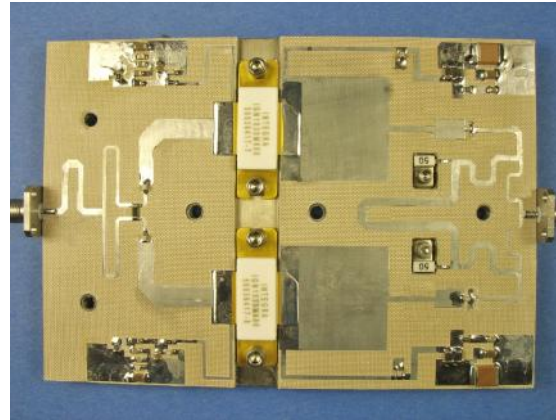


Part Number: IGNP1011M1600

L-Band Radar Pallet Amplifier

IGNP1011M1600 is a 50Ω matched, GaN-based pulsed power pallet amplifier for L-Band avionics systems operating in the 1030 -1090 MHz instantaneous frequency band. The pallet amplifier supplies a minimum of 1600 watts of peak pulsed output power under the conditions of 100μs pulse width and 2% duty cycle. All units are 100% screened for large signal RF parameters.



Integra

TECHNOLOGIES, INC.

GaN on Silicon Carbide FET

- High Power Gain
- Excellent Thermal Stability
- Gold Metal

Pulsed Operation

Class AB operation

- High Efficiency

Bias Sequencing required

- Negative gate voltage to Bias
- See App Note to Prevent Damage

Gold Metal System

- Maximum Reliability

Pallet Carrier

- Nickel-Plated Aluminum

TYPICAL DATA

TYPICAL DATA

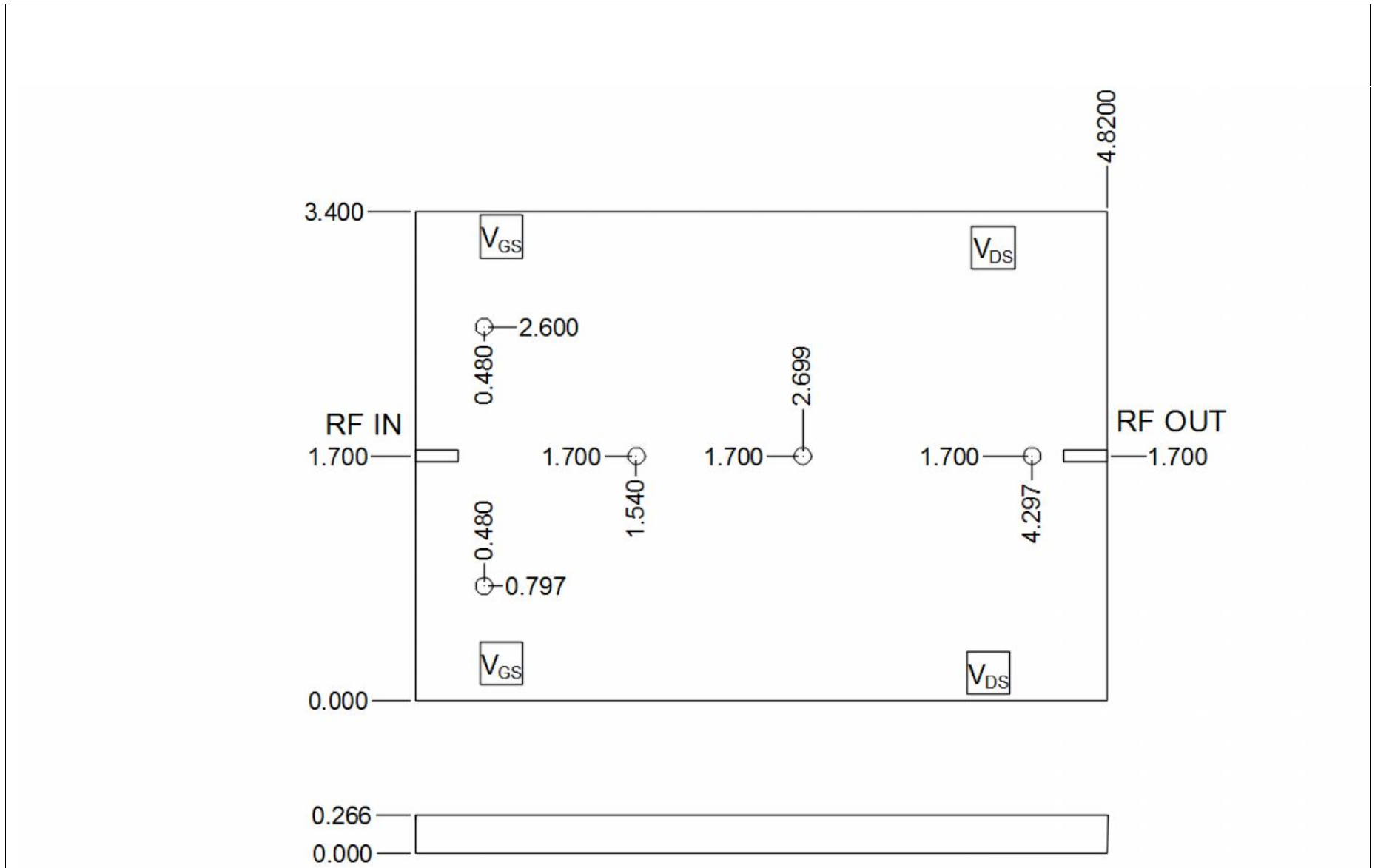
TYPICAL DATA

Pallet	Freq (MHz)	V _{DD} (V)	P _{IN} (W)	IRL (dB)	P _{OUT} (W)	G _P (dB)	I _d (A)	N _C (%)	Droop (dB)
50037562	1030	50	56.5	18.0	1883	15.23	65.25	57.74	-0.23
	1090	50	56.5	18.0	1893	15.25	59.50	63.64	-0.13

RF ELECTRICAL CHARACTERISTICS

Screen	Parameter	Symbol	Min	Max	Units	Test Conditions
100%	Input Return Loss	IRL	10	--	dB	$V_{DD}=V1, I_{DQ}=I_{DQ1}, PW=PW1, DF=DF1, T_F=25\pm5^\circ C, P_{IN}=P_{IN1}, F=F1, F2.$
100%	Output Power	P_{out}	1600	--	W	$V_{DD}=V1, I_{DQ}=I_{DQ1}, PW=PW1, DF=DF1, T_F=25\pm5^\circ C, P_{IN}=P_{IN1}, F=F1, F2.$
100%	Power Gain	G_P	14.50	--	dB	$V_{DD}=V1, I_{DQ}=I_{DQ1}, PW=PW1, DF=DF1, T_F=25\pm5^\circ C, P_{IN}=P_{IN1}, F=F1, F2.$
100%	Output Power Flatness 10*(Pout Max /Pout Min)	OPF	--	1.5	dB	$V_{DD}=V1, I_{DQ}=I_{DQ1}, PW=PW1, DF=DF1, T_F=25\pm5^\circ C, P_{IN}=P_{IN1}, F=F1, F2.$
100%	Efficiency ($P_o/I_c/V_{CC}$)	N_c	55	--	%	$V_{DD}=V1, I_{DQ}=I_{DQ1}, PW=PW1, DF=DF1, T_F=25\pm5^\circ C, P_{IN}=P_{IN1}, F=F1, F2.$
100%	Peak Current	I_d	--	65	A	$V_{DD}=V1, I_{DQ}=I_{DQ1}, PW=PW1, DF=DF1, T_F=25\pm5^\circ C, P_{IN}=P_{IN1}, F=F1, F2.$
100%	Pulse Amplitude Droop	Droop	-0.6	--	%	$V_{DD}=V1, I_{DQ}=I_{DQ1}, PW=PW1, DF=DF1, T_F=25\pm5^\circ C, P_{IN}=P_{IN1}, F=F1, F2.$
100%	Delta Insertion Phase Variation	d-IP	-20	+20	deg	$V_{DD}=V1, I_{DQ}=I_{DQ1}, PW=PW1, DF=DF1, T_F=25\pm5^\circ C, P_{IN}=P_{IN1}, F=F1, F2.$
100%	2:1 Load Mismatch Stability	VSWR-S	--	2:1	--	$V_{DD}=V1, I_{DQ}=I_{DQ1}, PW=PW1, DF=DF1, T_F=25\pm5^\circ C, P_{IN}=P_{IN1}, F=F1, F2.$ Rotate 2:1 output VSWR through 360° phase. No oscillatory or pulse break-up characteristics allowed on detected output pulse. All non-harmonically related signals must be at least -60 dBc.
100%	Load Mismatch Tolerance	LMT	--	5:1	--	$V_{DD}=V1, I_{DQ}=I_{DQ1}, PW=PW1, DF=DF1, T_F=25\pm5^\circ C, P_{IN}=P_{IN1}, F=F1, F2.$ Rotate 5:1 output VSWR through 360° phase. Post test P_o = Pre test $P_o \pm 5W$
Note 1	$V1 = 50V; I_{DQ1} = 200mA; PW1 = 100us; DF1 = 2%, P_{IN1} = 56.50 W$					
Note 2	Test Frequencies: F1 = 1030 MHz, F2 = 1090 MHz.					
Note 3	$T_{F1} = 25\pm5^\circ C$ = Device flange temperature.					
Note 4	Screen 'BD' = parameter qualified By Design.					

PALLET DIMENSIONAL OUTLINE DRAWING



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Data Sheet Status	
Proposed Specification	This data sheet contains proposed specifications.
Preliminary Specification	This data sheet contains specifications based on preliminary measurements and data.
Product Specification	This data sheet contains final product specifications.
Maximum Ratings	
Stress above one or more of the maximum ratings may cause permanent damage to the device. These are maximum ratings only operation of the device at these or at any other conditions above those given in the characteristics sections of the specification is not implied. Exposure to maximum values for extended periods of time may affect device reliability.	

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