

## P-Band, GaN/SiC, RF Power Amplifier Pallet

400-450 MHz | 1kW typ | 75% Efficiency typ | 20 dB Gain typ | 50 V | 300 µs Pulse Length, 10% Duty Cycle

IGNP0450M850 is a high power GaN-on-SiC RF power amplifier pallet that has been designed to suit the unique needs of P band radar systems. It operates over the full 400-450 MHz frequency range. Under 300µs, 10% duty cycle pulse conditions, it supplies a minimum of 850 W of peak output power, with typically >20 dB  $^{50\Omega}$ of gain and 75% efficiency. It operates from a 50 V supply voltage.

# Output Matching Matching Circuit Circuit

**Block Diagram of IGNP0450M850** 

#### **FEATURES**

- GaN on SiC HEMT Technology
- Output Power >850W
- Fully matched to  $50\Omega$
- Exceptionally High Efficiency up to 78%
- 100% RF Tested Under 300μs, 10% duty cycle pulse conditions

#### **APPLICATIONS**

P-band Radar Systems

Table 1. RF Electrical Characteristics (Case temperature = 30 °C unless otherwise stated)

Parameter	Symbol	Min	Тур	Max	Units	Test Conditions	
RF Ouput Power	P <sub>out, RF</sub>	850	1000	1150	W	P <sub>IN</sub> = 9W	
Gain	G	19.8	20.5	21.8	dB	f = 400, 425, 450 MHz	
Drain Efficiency	η	70	75	85	%		
Pulse Droop	D	-0.4	-0.1	+0.2	dB	300μs pulse length, 10% duty cycle	
Load Mismatch Stability	VSWR-S	2:1				$V_{DS} = 50V, I_{DS} = 150mA,$	
VSWR Withstand	VSWR-LMT	3:1					

Note: Consult Integra Technologies Application Note 001 for information on how RF output power and pulse droop are measured.



**Table 2. Absolute Maximum Ratings (Not Simultaneous)** 

Parameter	Symbol	Value	Units	Test Conditions
DC Drain-Source Supply Voltage	V <sub>DS</sub>	50	V	25 °C
DC Gate-Source Voltage	V <sub>GS</sub>	-8 to +1.0	V	25 °C
DC Drain Current	I <sub>D</sub>	81	A	25 °C
DC Gate Current	I <sub>G</sub>	81	mA	25 °C
RF Input Power	P <sub>RE,IN</sub>	9	W	25 °C
Operating Ambient Temperature	T <sub>AMB</sub>	-40 to +85	°C	
Storage Temperature	T <sub>stg</sub>	-55 to +150	°C	
Amplifier Pallet Soldering temperature	T <sub>SOLDER</sub>	280	°C	<50 sec at >200 °C
Operating Channel Temperature	T <sub>CH</sub>	-55 to +200	°C	
Storage Temperature	T <sub>stg</sub>	-55 to +150	°C	
Soldering Temperature	T <sub>SOLDER</sub>	260 for 60s	°C	

Note: Operation outside the limits given in this table may cause permanent damage

Table 3. DC Electrical Characteristics (Case temperature = 25 °C unless otherwise stated)

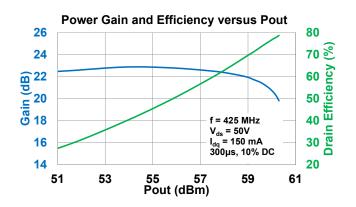
Parameter	Symbol	Min	Тур	Max	Units	Test Conditions
Gate Pinch-Off Voltage	V <sub>P</sub>	-5.0			V	$V_{DS} = 50V$ , $I_{DS} = 1mA$
Quiescent Gate Voltage	V <sub>Q</sub>		-2.9		V	$V_{DS} = 50V, I_{DS} = 150mA$

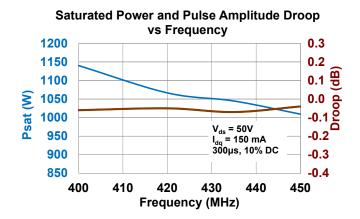
Table 4. Thermal Resistance (Case temperature = 25 °C unless otherwise stated)

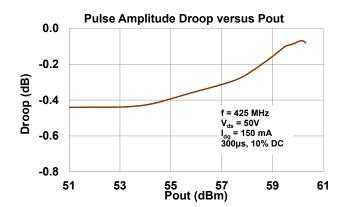
Parameter	Symbol	Min	Тур	Test Conditions
Peak Thermal Resistance, channel to underneath side of Amplifier Pallet	R <sub>TH</sub>		TBD	$P_{OUT} = 850W$ f = 425  MHz $300\mu \text{s pulse length, } 10\% \text{ duty cycle}$ $V_{DS} = 50V, I_{DS} = 150\text{mA}$

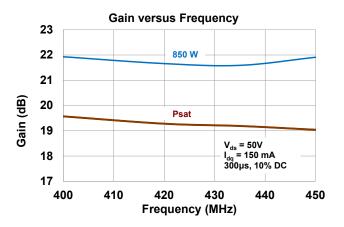


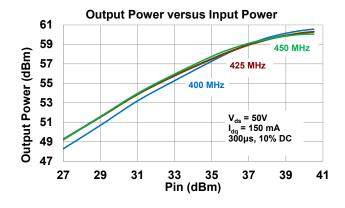
#### **TYPICAL PERFORMANCE**

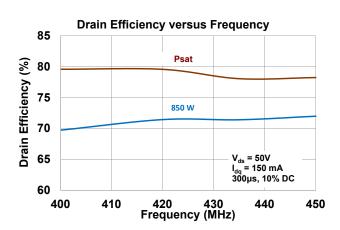






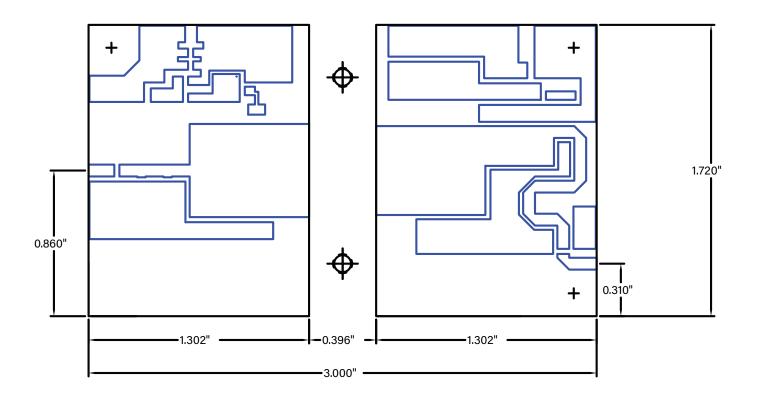








### **DIMENSIONS**





#### **ESD Rating**

Parameter	Rating	Standard
ESD Human Body Model (HBM)	TBD	ESDA/JEDEC JS-001-2012
ESD Charged Device Model (CDM)	TBD	JEDEC JESD22-C101F
Moisture Sensitivty Level (MSL)	0	IPC/JEDEC J-STD-020

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