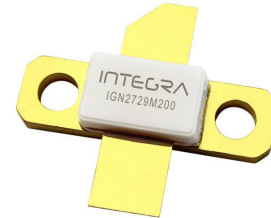


S-Band, GaN/SiC, RF Power Transistor

2.7 - 2.9 GHz | 200 W typ | 65% Efficiency typ | 18dB Gain typ | 50 V | 100µs Pulse Length, 10% Duty Cycle

IGN2729M200 is a high power GaN-on-SiC RF power transistor that has been designed to suit the unique needs of modern radar systems. It supplies 200W of peak output power, with typically >18 dB of gain and 65% efficiency. It operates from a 50 V supply voltage. For optimal thermal efficiency, the transistor is housed in a metal-based package with an epoxy-sealed ceramic lid.



FEATURES

- GaN on SiC HEMT Technology
- Output Power 200W
- Pre-matched Input Impedance
- High Efficiency - 65% typical
- 100% RF Tested Under 100µs, 10% duty cycle pulse conditions
- RoHS and REACH Compliant
- Full non-linear electrothermal model available, please contact the factory.

APPLICATIONS

- S-band Radar Systems

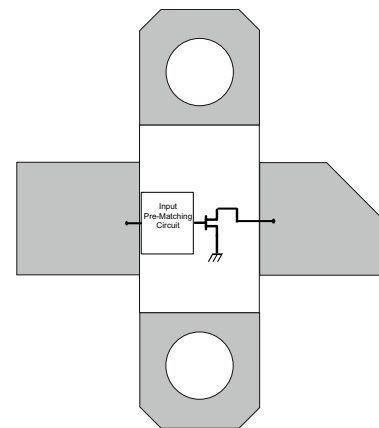


Table 1. RF Electrical Characteristics (Case temperature = 30 °C unless otherwise stated)

Parameter	Symbol	Min	Typ	Max	Units	Test Conditions
RF Input Power	$P_{IN,RF}$	2	3.2	4	W	$P_{OUT} = 200W$ $f = 2.7, 2.8, 2.9 GHz$ 100µs pulse length, 10% duty cycle $V_{DS} = 50V, I_{DS} = 20mA$
Gain	G	17	18	20	dB	
Drain Efficiency	η	62	65	70	%	
Pulse Droop	D	-0.4	-0.25	0.2	dB	
Input Return Loss	IRL	10	20	25	dB	
Load Mismatch Stability	VSWR-S	3:1				
VSWR Withstand	VSWR-LMT	5:1				

Note: Consult Integra Technologies Application Note 001 for information on how RF output power and pulse droop are measured.

Table 2. DC Electrical Characteristics (Case temperature = 25 °C unless otherwise stated)

Parameter	Symbol	Min	Typ	Max	Units	Test Conditions
Gate Pinch-Off Voltage	V_P	-5.0			V	$V_{DS} = 50V, I_{DS} = 1mA$
Quiescent Gate Voltage	V_Q		-2.7		V	$V_{DS} = 50V, I_{DS} = 20mA$

Table 3. Absolute Maximum Ratings (Not Simultaneous)

Parameter	Symbol	Value	Units	Test Conditions
DC Drain-Source Voltage	V_{DS}	140	V	25 °C
DC Gate-Source Voltage	V_{GS}	-8 to +1.5	V	25 °C
DC Drain Current	I_D	13	A	25 °C
DC Gate Current	I_G	1.3	mA	25 °C
RF Input Power	P_{RFIN}	4	W	25 °C
Operating Channel Temperature	T_{CH}	-55 to +225	°C	
Storage Temperature	T_{STG}	-55 to +150	°C	26.4mm
Soldering Temperature	T_{SOLDER}	260 for 60s	°C	

Note: Operation outside the limits given in this table may cause permanent damage to the transistor

Table 4. Thermal Resistance (Case temperature = 85 °C unless otherwise stated)

Parameter	Symbol	Min	Typ	Max	Units	Test Conditions
Peak Thermal Resistance, Channel to Case	R_{TH}		0.67		°C/W	$P_{DISS} = 107.7W$ 100µs pulse length, 10% duty cycle $V_{DS} = 50V$

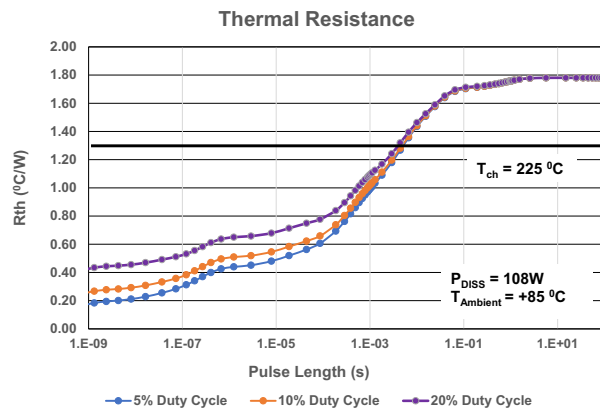
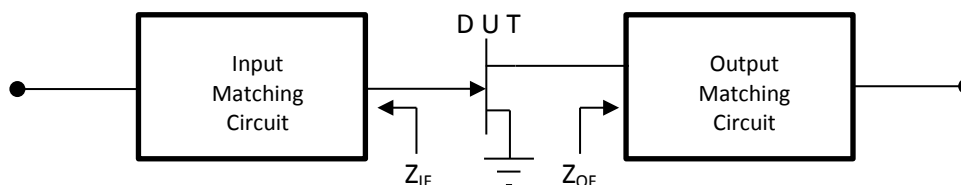


Table 5. Optimum Source & Load Impedances (Case temperature = 25 °C unless otherwise stated)

Frequency (GHz)	Z_{IF}	Z_{OF}	Units	Test Conditions
2.7	2.5 - j5.3	2.7 - j2.3	Ω	$P_{OUT} = 200W$ 100µs Pulse length, 10% Duty Cycle $V_{DS} = 50V, I_{DS} = 20mA$
2.8	2.4 - j4.8	2.6 - j2.6	Ω	
2.9	2.4 - j4.3	2.6 - j2.9	Ω	



TYPICAL PERFORMANCE

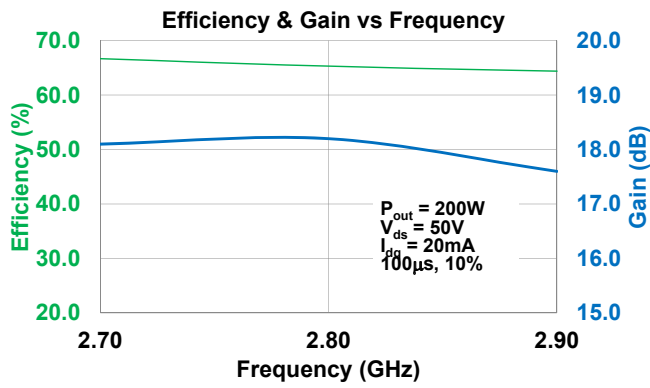


Figure 1

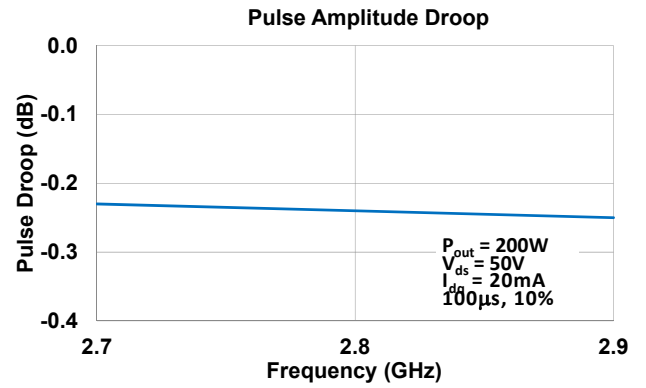


Figure 2

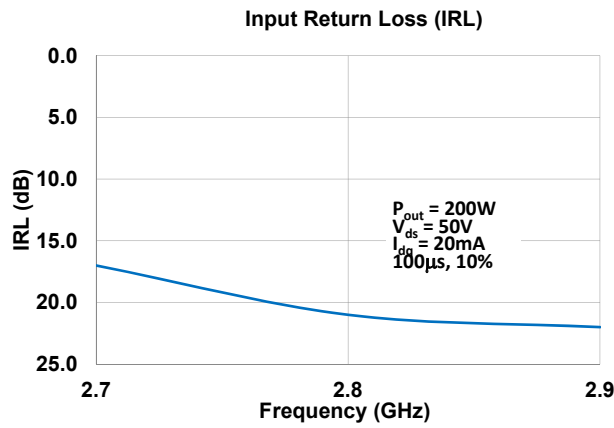
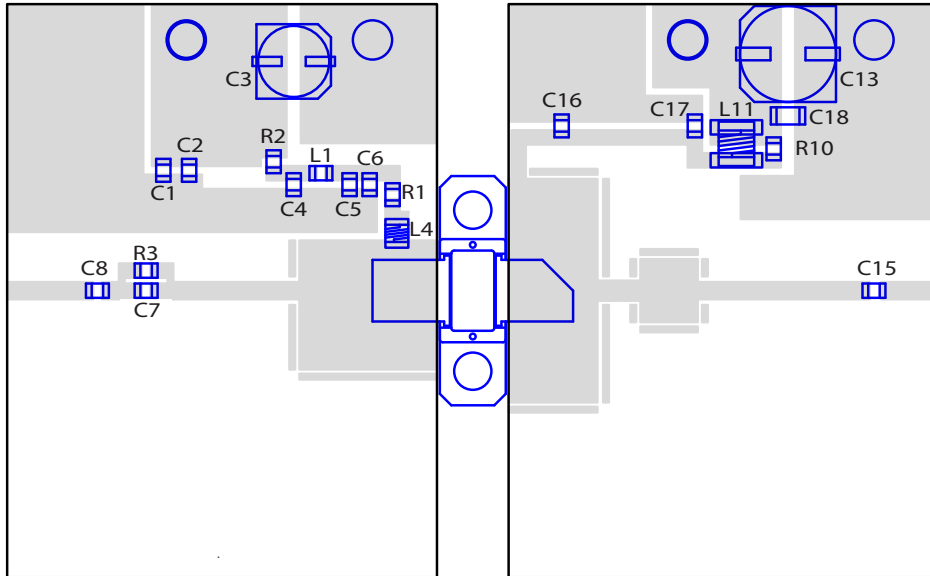


Figure 3

TEST FIXTURE

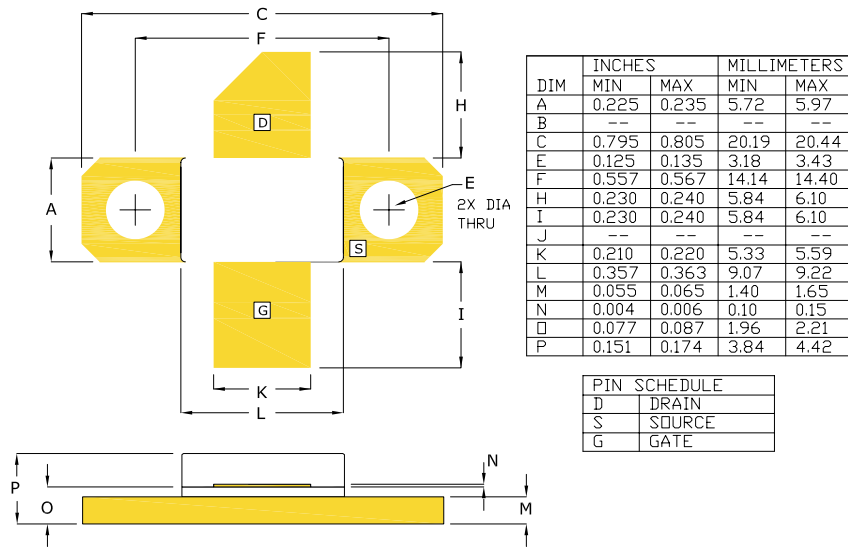


Note: It is recommended that a 4700 μ F 63V electrolytic capacitor be connected between ground and the positive supply terminal of the test fixture, and placed as close as possible to the test fixture, in order to minimise pulse droop.

Bill of Materials for IGN2729M200 Test Fixture

Designator	Description	Part Number
C1, C4	CAP 0.1 μ F, 0805, 50V	C0805C104K5RACTU
C2,C5	CAP 1000pF, 0805, 50V	C0805C102M5RACTU
C3	CAP 47 μ F, 25V Electrolytic	PCV1E470MCL2GS
C6, C7, C8, C15, C17	CAP 10pF, 0805, Edge Mount	600F100JT250XT
C13	CAP 68 μ F, 63V, Electrolytic	EEE-FK1J680P
C16	CAP 5.6pF, 0805, Edge Mount	600F5R6CT250T
C18	CAP 1 μ F, 1206, X7R, 50V	C1206C105K5RACTU
C19 (See note above)	CAP 4700 μ F, 63V, Electrolytic	UVR1J472MRD6
L1	IND FB, 120 OHM, 0805 5A	ILHB0805ER121V
L4	IND 27.3nH, 0908	0908SQ-27N
L11	IND 5.5nH, 1508	1508-5N5JLB
R1, R10	RES 10, 0805	ERJ-6ENF10R0V
R2	RES 200, 0805	ERJ-6ENF2000V
R3	RES 51, 0805	ERJ-6GEYJ510V
PC Board Type	ROGERS R04350B-030-1/1, 30mil, 1/1oz. Copper	

PACKAGE PL32A1



Dimensions

ESD & MSL Rating

Parameter	Rating	Standard
ESD Human Body Model (HBM)	TBD	ESDA/JEDEC JS-001-2012
ESD Charged Device Model (CDM)	TBD	JEDEC JESD22-C101F
Moisture Sensitivity Level (MSL)	Unlimited Shelf Life	IPC/JEDEC J-STD-020

RoHS Compliance

Integra Technologies, Inc declares that its GaN and LDMOS Transistor Products comply with EU Directive 2011/65/EU on the restriction of the use of certain hazardous substances in electrical and electronic equipment (RoHS2), as adopted by EU member states on January 2, 2013 and amended on March 31, 2015 by EU Directive 2015/863/EU.

REACH Compliance

Integra Technologies supports EU Regulation number 1907/2006 concerning the Registration, Evaluation, Authorization, and Restriction of Chemicals (REACH) as these apply to Integra semiconductor products, development tools, and shipping packaging.

In support of the REACH regulation, Integra will:

- Inform customers and recipients of Integra product if they contain any substances that are of very high concern (SVHC) per the European Chemical Agency (ECHA) website.
- Notify ECHA if any Integra product that contains any SVHCs which exceed guidelines for REACH chemicals by weight per part number and for total content weight per year for all products produced in or imported to the European market.
- Cease shipments of product containing REACH Annex XIV substances until authorization has been obtained.
- Cease shipment of product containing REACH Annex XVII chemicals when restrictions apply.

Integra has evaluated its materials, BOMs, and product specifications and product and has determined that this transistor conforms to all REACH and SVHC regulations and guidelines. Integra has implemented actions and control programs that will assure continued compliance.

Disclaimer

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DEFINITIONS:

DATA SHEET STATUS

Advanced Specification - This data sheet contains Advanced specifications.

Preliminary Specification - This data sheet contains specifications based on preliminary measurements and data.

Final Specification - This data sheet contains final product specifications.

MAXIMUM RATINGS Stress above one or more of the maximum ratings may cause permanent damage to the device. These are maximum ratings only operation of the device at these or at any other conditions above those given in the characteristics sections of the specification is not implied. Exposure to maximum values for extended periods of time may affect device reliability.

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