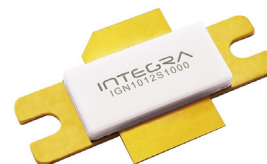


# L-Band, GaN/SiC, RF Power Transistor

1.025 - 1.15 GHz | 1000 W typ | 65% Efficiency typ | 15.5 dB Gain typ | 50 V | 32μs Pulse Length, 2% Duty Cycle

IGN1012S1000 is a high power GaN-on-SiC RF power transistor that has been designed to suit the unique needs of L-band radar systems. It supplies a minimum of 1000 W of peak output power, with typically >15.5 dB of gain and 65% efficiency. It operates from a 50 V supply voltage. For optimal thermal efficiency, the transistor is housed in a metal-based package with an epoxy-sealed ceramic lid.



## FEATURES

- GaN on SiC HEMT Technology
- Output Power >1000 W
- Pre-matched Input Impedance
- High Efficiency - up to 68%
- 100% RF Tested Under 32μs, 2% duty cycle pulse conditions
- RoHS and REACH Compliant

## APPLICATIONS

- L-band Radar Systems

**Table 1. Absolute Maximum Ratings (Not Simultaneous)**

Parameter	Symbol	Value	Units	Test Conditions
DC Drain-Source Voltage	$V_{DS}$	150	V	25 °C
DC Gate-Source Voltage	$V_{GS}$	-10 to 0	V	25 °C
DC Drain Current	$I_D$	72	A	25 °C
DC Gate Current	$I_G$	72	mA	25 °C
RF Input Power	$P_{RF,IN}$	40	W	25 °C
Operating Junction Temperature	$T_J$	-55 to +200	°C	
Storage Temperature	$T_{STG}$	-55 to +150	°C	
Soldering Temperature	$T_{SOLDER}$	260 for 60s	°C	

Note: Operation outside the limits given in this table may cause permanent damage to the transistor

**Table 2. DC Electrical Characteristics (Case temperature = 25 °C unless otherwise stated)**

Parameter	Symbol	Min	Typ	Max	Units	Test Conditions
Gate Pinch-Off Voltage	$V_P$	-5.0			V	$V_{DS} = 50V, I_{DS} = 1mA$
Quiescent Gate Voltage	$V_Q$		-2.5		V	$V_{DS} = 50V, I_{DS} = 75mA$

**Table 3. RF Electrical Characteristics (Case temperature = 30 °C unless otherwise stated)**

Parameter	Symbol	Min	Typ	Max	Units	Test Conditions
Input Return Loss	IRL	10	14	18	dB	$P_{OUT} = 1000W$ $f = 1.025, 1.090, 1.150 \text{ GHz}$ 32 $\mu$ s pulse length 2% duty cycle pulse conditions $V_{DS} = 50V, I_{DS} = 75mA,$
RF Input Power	$P_{IN,RF}$	20.0	28.2	32.0	W	
Gain	G	15.0	15.5	17.0	dB	
Drain Efficiency	$\eta$	55	65	75	%	
Pulse Droop	D	-0.4	-0.2	+0.2	dB	
Load Mismatch Stability	VSWR-S	2:1				
VSWR Withstand	VSWR-LMT	3:1				

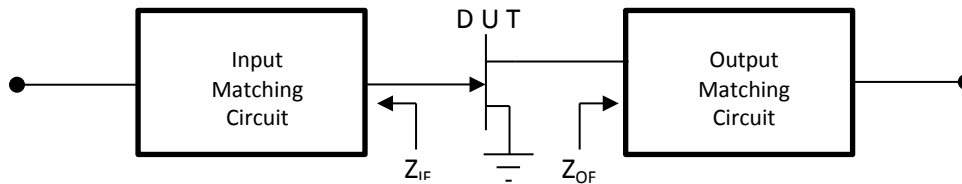
Note: Consult Integra Technologies Application Note 001 for information on how RF output power and pulse droop are measured.

**Table 4. Thermal Resistance (Case temperature = 25 °C unless otherwise stated)**

Parameter	Symbol	Min	Typ	Max	Units	Test Conditions
Peak Thermal Resistance, Junction to Case	$R_{TH(JC)}$			0.1	°C/W	$P_{OUT} = 1000W$ $f = 1.025, 1.090, 1.150 \text{ GHz}$ 32 $\mu$ s pulse length 2% duty cycle $V_{DS} = 50V, I_{DS} = 75mA$

**Table 5. Optimum Source & Load Impedances (Case temperature = 25 °C unless otherwise stated)**

Frequency (GHz)	$Z_{IF}$	$Z_{OF}$	Units	Test Conditions
1.025	4.0 - j 0.9	1.1 + j 0.45	$\Omega$	$P_{OUT} = 1000W$ 32 $\mu$ s pulse length 2% duty cycle $V_{DS} = 50V, I_{DS} = 75mA$
1.090	4.25 - j 0.1	1.0 + j 0.5	$\Omega$	
1.150	4.6 + j 0.65	0.8 + j 0.6	$\Omega$	



TYPICAL PERFORMANCE

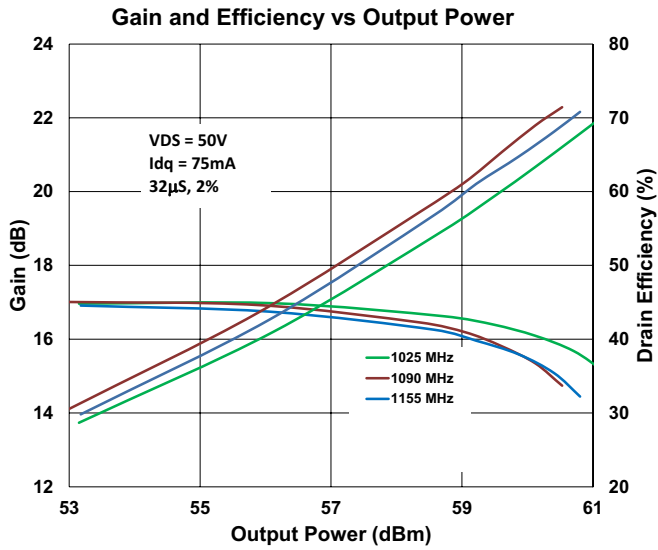


Figure 1

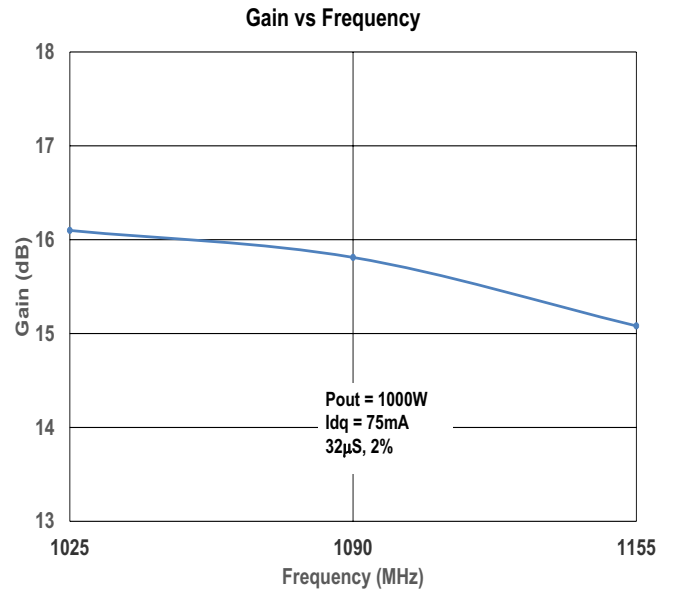


Figure 2

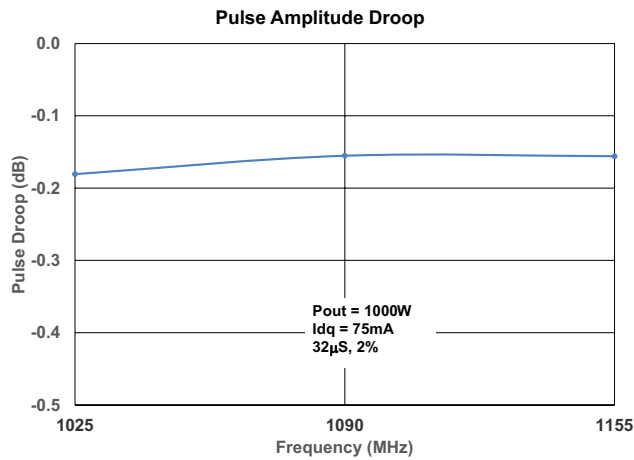
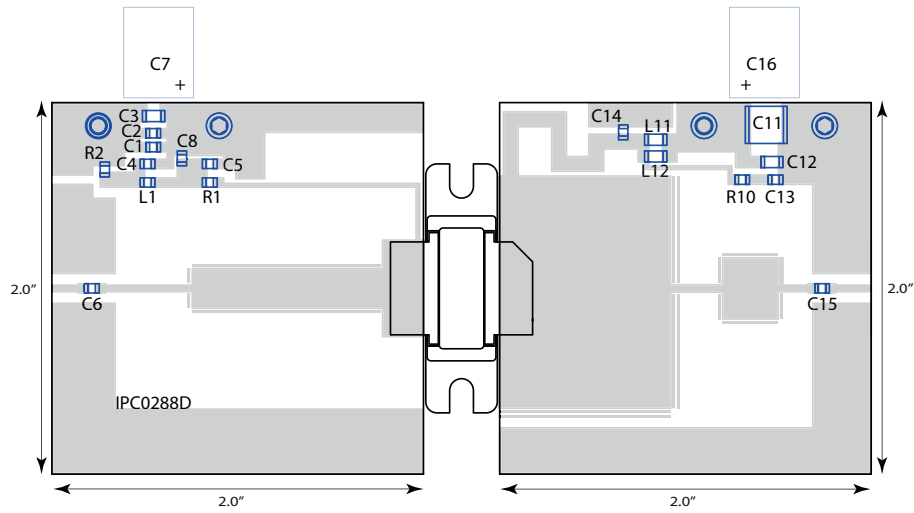


Figure 3

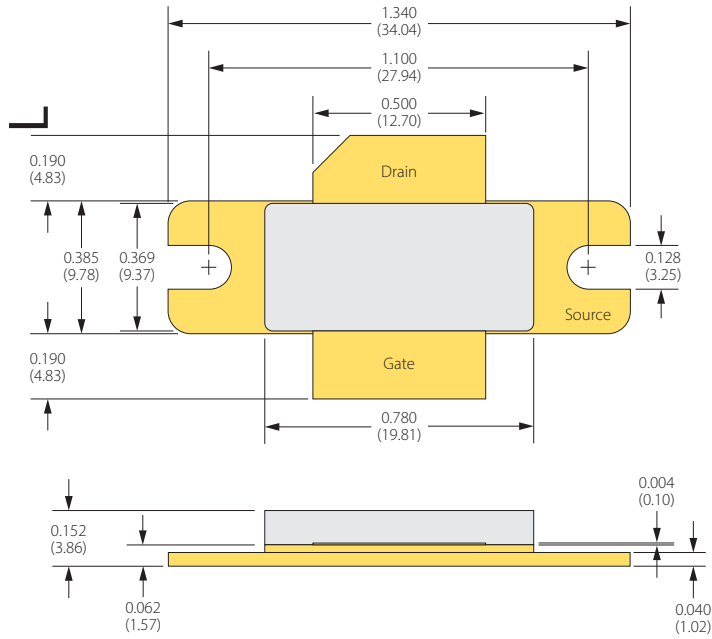
**TEST FIXTURE**



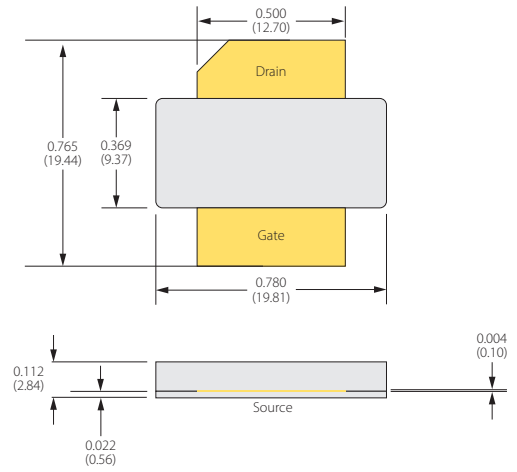
**Bill of Materials for IG1012S1000 Test Fixture**

Designator	Description	Quantity	Part Number
C1, C4, C13	CAP 0.1 $\mu$ F, 0805, 50V	3	08051C104K4T2A
C2,C5,C6,C14,C15	CAP 33pF, 0805	5	ATC600F330
C3 ,C12	CAP 1 $\mu$ F, 1206	2	12061C105K4T2A
C7, C16	CAP 68 $\mu$ F, 63V, Electrolytic	2	UPW1J680MPD
C8	CAP 1000pF, 0805, 50V, X7R	1	08051A102J4T2A
C11	CAP 10 $\mu$ F, 2220, 50V, X7R	1	22201C106MAT2A
L1	IND FB, 120 OHM, 0805, 5A	1	ILHB0805ER121V
L11, L12	IND FB, 33 OHM, 1206, 6A	2	BLM31PG330SN1L
R1, R10	RES, 15 OHM, 0805	2	ERJ-6ENF150V
R2	RES, 100 OHM, 0805	1	ERJ-6ENF1000V
PC Board Type	ROGERS RT6006, 25mil, 1/1oz. Copper	2	

**PACKAGE PL84A1**



**BOLT-DOWN FLANGE OPTION  
IGN1012S1000**



**EARLESS FLANGE OPTION  
IGN1012S1000S**

**Dimensions: Inches (mm)**

### ESD & MSL Rating

Parameter	Rating	Standard
ESD Human Body Model (HBM)	TBD	ESDA/JEDEC JS-001-2012
ESD Charged Device Model (CDM)	TBD	JEDEC JESD22-C101F
Moisture Sensitivity Level (MSL)	Unlimited Shelf Life	IPC/JEDEC J-STD-020

### RoHS Compliance

Integra Technologies, Inc declares that its GaN and LDMOS Transistor Products comply with EU Directive 2011/65/EU on the restriction of the use of certain hazardous substances in electrical and electronic equipment (RoHS2), as adopted by EU member states on January 2, 2013 and amended on March 31, 2015 by EU Directive 2015/863/EU.

### REACH Compliance

Integra Technologies supports EU Regulation number 1907/2006 concerning the Registration, Evaluation, Authorization, and Restriction of Chemicals (REACH) as these apply to Integra semiconductor products, development tools, and shipping packaging.

In support of the REACH regulation, Integra will:

- Inform customers and recipients of Integra product if they contain any substances that are of very high concern (SVHC) per the European Chemical Agency (ECHA) website.
- Notify ECHA if any Integra product that contains any SVHCs which exceed guidelines for REACH chemicals by weight per part number and for total content weight per year for all products produced in or imported to the European market.
- Cease shipments of product containing REACH Annex XIV substances until authorization has been obtained.
- Cease shipment of product containing REACH Annex XVII chemicals when restrictions apply.

Integra has evaluated its materials, BOMs, and product specifications and product and has determined that this transistor conforms to all REACH and SVHC regulations and guidelines. Integra has implemented actions and control programs that will assure continued compliance.

### Disclaimer

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#### DEFINITIONS:

##### DATA SHEET STATUS

Advanced Specification - This data sheet contains Advanced specifications.

Preliminary Specification - This data sheet contains specifications based on preliminary measurements and data.

Final Specification - This data sheet contains final product specifications.

**MAXIMUM RATINGS** Stress above one or more of the maximum ratings may cause permanent damage to the device. These are maximum ratings only operation of the device at these or at any other conditions above those given in the characteristics sections of the specification is not implied. Exposure to maximum values for extended periods of time may affect device reliability.